



C 75
PATENT
Attorney's Doc. No. 8371-129

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No.: US 6,900,083 B2
Inventor: Voutsas et al.
Issued: May 31, 2005

For: METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM
TRANSISTOR

Ser. No.: 09/945,063 Filed: August 31, 2001
Group Art Unit: 2823 Examiner: George Fourson

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Enclosed for filing are the following:

Request for Certificate of Correction
 Certificate of Correction Form PTO 1050 (in duplicate)
 Return postcard
 Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Certificate
OCT 27 2006
of Correction

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

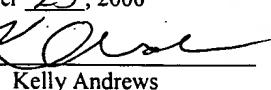


Hosoon Lee
Reg. No. 56,737

MARGER JOHNSON & McCOLLOM, P.C.
210 SW Morrison Street, Suite 400
Portland, OR 97204
503-222-3613

Customer No. 46404

I hereby certify that this correspondence
is being deposited with the United States
Postal Service as first class mail in an
envelope addressed to: Commissioner for
Patents, P.O. Box 1450, Alexandria, VA
22313-1450
Date: October 23, 2006


Kelly Andrews



PATENT
Attorney's Doc. No. 8371-129

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No.: US 6,900,083 B2
Inventor: Voutsas et al.
Issued: May 31, 2005

For: METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM
TRANSISTOR

Ser. No.: 09/945,063 Filed: August 31, 2001
Group Art Unit: 2823 Examiner: George Fourson

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Upon comparison of the above-identified original Letters Patent with applicant's copy of the application therefore, the following error in the original Letters Patent has been noted:

On the cover page, the title reads "METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM TRANSISTOR" please replace it with
--METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM
TRANSISTOR (TFT) AND THE DEVICE FORMED THEREBY--

Applicant respectfully requests that a Certificate of Correction calling attention to the above error be issued pursuant to Rule 322 of the Patent Office and Title 35, United States Code, Section 254. Two copies of a Certificate of Correction form are enclosed.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

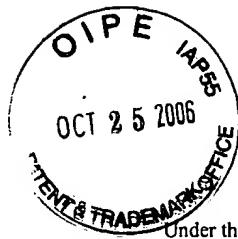
Hosoon Lee
56,737

MARGER JOHNSON & McCOLLOM, P.C.
210 SW Morrison Street, Suite 400
Portland, OR 97204
503-222-3613

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Date: October 23, 2006

Kelly Andrews



PTO/SB/44 (02-01)

Approved for use through 01/31/2004. OMB 0651-0033

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : US 6,900,083 B2
DATED : May 31, 2005
INVENTOR(S) : Voutsas et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the cover page, the title reads "METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM TRANSISTOR" please replace it with --METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM TRANSISTOR (TFT) AND THE DEVICE FORMED THEREBY--

MAILING ADDRESS OF SENDER:
Marger Johnson & McCollom
210 S.W. Morrison Street, Suite 400
Portland, Oregon 97204

PATENT NO. US 6,900,083-B2

No. of additional copies

1

Burden Hour Statement: This form is estimated to take 1.0 hour to complete. Time will vary depending upon the needs of the individual case. Any comment on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, D.C. 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, D.C. 20231.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : US 6,900,083 B2
DATED : May 31, 2005
INVENTOR(S) : Voutsas et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the cover page, the title reads "METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM TRANSISTOR" please replace it with --METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM TRANSISTOR (TFT) AND THE DEVICE FORMED THEREBY--

MAILING ADDRESS OF SENDER:
Marger Johnson & McCollom
210 S.W. Morrison Street, Suite 400
Portland, Oregon 97204

PATENT NO. US 6,900,083 B2

No. of additional copies

1

Burden Hour Statement: This form is estimated to take 1.0 hour to complete. Time will vary depending upon the needs of the individual case. Any comment on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, D.C. 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, D.C. 20231.